

EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	2479	(board card) near10 (row slot) same (chassis (circuit near3 pack))	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:35
L2	6	(board card) near10 (row slot) near10 (chassis cage cabinet) near10 (circuit near3 pack)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:46
L3	7	(board card) near10 (row slot) near10 (assembly chassis cage cabinet) near10 (circuit near3 pack)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:51
L4	14	(board card) near10 (row slot) same (assembly chassis cage cabinet) near10 (circuit near3 pack)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:52
L5	163	(board card) near10 (row slot) same (assembly chassis cage cabinet) near10 pack\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:54
L6	27769	"710"/\$.ccls. and5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:52
L7	4	"710"/\$.ccls. and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:52
L8	41	"361"/\$.ccls. and 5	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:55
L9	178	(board card pcb) near10 (row slot) same (assembly chassis cage cabinet) near10 pack\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:00

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L10	44	"361"/\$.ccls. and 9	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:01
L11	578	(board card pcb module pack) near10 (row slot) same (assembly chassis cage cabinet backplane) near10 pack\$3	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:00
L12	1300	(board card pcb module pack) near10 (row slot) same (assembly chassis cage cabinet backplane) near10 (interconnect\$3 pack\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:36
L13	228	"361"/\$.ccls. and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:37
L14	112	"710"/\$.ccls. and 12	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:37
L15	1428	(board card pcb module pack blade) near10 (row slot) same (assembly chassis cage cabinet backplane) near10 (interconnect\$3 pack\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:40
L16	114	"710"/\$.ccls. and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:37
L17	235	"361"/\$.ccls. and 15	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 10:37
L18	36	(board card pcb module pack blade) near10 (row and slot) same (assembly chassis cage cabinet backplane) near10 (interconnect\$3 pack\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 11:57
L19	0	(board card pcb module pack blade) near10 (row and slot) same (mesh\$3 near3 interconnect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 11:58

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L20	0	(board card pcb module pack blade) near10 (row and slot) same (mesh\$3 near10 interconnect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 11:58
L21	24	(board card pcb module pack blade) near10 (row and slot) and (mesh\$3 near10 interconnect\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 11:58
S1	21203889	@ad<"20010928"	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2004/06/08 16:26
S2	0	last-first\$.in.	US-PGPUB; USPAT	OR	ON	2003/09/29 12:04
S3	2	ep-804933-\$.did.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:33
S4	15898	"710"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2006/11/22 09:52